

P20363.A01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Seung-Won CHOI

Group Art Unit: 2661

Serial No. : 09/734,670

Examiner:

Filed : December 13, 2000

For : SIGNAL PROCESSING METHOD FOR USE IN AN ARRAY ANTENNA  
SYSTEM IN CDMA MOBILE TELECOMMUNICATIONS NETWORK  
AND RECORDING MEDIUM THEREFOR

**INFORMATION DISCLOSURE STATEMENT**

Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure under 37 C.F.R. 1.56, 1.97-1.98, Applicant hereby calls the following document to the Examiner's attention:

T.A. DENIDNI and G.Y. DELISLE, "A Nonlinear Algorithm for Output Power Maximization of an Indoor Adaptive Phased Array", IEEE Transactions on Electromagnetic Compatibility, Vol. 37, No. 2, pp. 201-209 (May 1995).


Applicant respectfully requests that the Examiner consider and cite the above article. A copy of the above-noted document is attached and has been listed on a PTO-1449 Form which is also attached hereto. Accordingly, the Examiner is requested to initial the appropriate space on the attached PTO-1449 Form and to return a copy of the Form to the

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Applicant with the next official communication in the present application to confirm consideration of this document.

Should the Examiner have any questions, the Examiner is invited to contact the undersigned at the below-listed telephone number.

Respectfully submitted,  
Seung-Won CHOI

  
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Reg. No. 29,027

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April 9, 2001  
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